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(VLSI.331PA)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JC986 U.S. PTO
10/005551
11/08/01

11/08/01
J1060 U.S. PTO

Applicant: Dutta et al.

Docket: US018181

Title: HIGH-SPEED COMPUTATION IN ARITHMETIC LOGIC CIRCUIT

CERTIFICATE UNDER 37 CFR 1.10

Express Mail' mailing label number: EL 735813432 US

Date of Deposit: November 8, 2001

I hereby certify that this paper or fee is being deposited with the United States Postal Service 'Express Mail Post Office To Addressee' service under 37 CFR 1.10 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

By:

Name: Kelly S. Waltigney

Kelly S. Waltigney

BOX PATENT APPLICATION

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

We are transmitting herewith the attached:

- ☒ Transmittal sheet containing Certificate under 37 CFR 1.10.
- ☒ Patent Application: Pages Numbered 1-27, including 1 page of Abstract; and 20 claims
- ☒ 5 sheets of informal drawings
- ☒ An executed Assignment and Declaration
- ☒ Assignment of the invention to Koninklijke Philips Electronics N.V. Recordation Form Cover Sheet
- ☒ Enclosed find two checks in the amounts of \$824.00 in payment of the filing fee and \$40.00 in payment of the assignment recordation.
- ☒ If necessary, charge/credit Deposit Account No. 50-0996 (VLSI.331PA) for any deficiencies/overpayments.
- ☒ 1 Return Postcard

The fees are calculated as follows:

Assignment Recordation Fee = \$40.00

Basic Filing Fee = \$740.00

Fee for total number of claims in excess of 20 = $\$18 * (20-20) = 0.00$

Fee for total number of independent claims in excess of 3 = $\$84 * (4 - 3) = 84.00$

Total filing fee due = \$824.00

Direct all correspondence to the address below:

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By:

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